

**Experienced Semiconductor Device/Test Engineer**

**Based: Fife, Scotland – KY5**

**Salary Commensurate with Experience**

**Hours per week: 40 hours per week**

**About Us:**

Beginning life as a new venture in 2017 with some \$50m in investment, here at Clas-SiC Wafer Fab Limited, we have completed the construction of a dedicated manufacturing facility for Silicon Carbide Power Semiconductors in Lochgelly, Fife and production is now well under way.

We have committed to using the Manufacturing Excellence framework as part of our drive to achieve early world class recognition with our unique value proposition of fast prototyping cycle times, enabling quick routes to market for our customers. Our values including 'Customers First'; 'Honesty & Integrity'; 'Open Communication'; 'Manage by the Facts' and 'Teamwork' help us to focus on what matters for our customers and our employees, every day.

As a result of our continuing progress, we now require an **Experienced Semiconductor Device/Test Engineer** for our wafer fab test area, to develop and implement test solutions for our diodes and MOSFETs. You will be familiar with setting up and operating manual and automatic testers and probers for high voltage/high current semiconductor wafer testing. Ideally you will also be familiar with reliability test routines and equipment for packaged power semiconductor devices. An understanding of Silicon Carbide wafer fabrication would be an advantage. A key skill will be the ability to manage the large volumes of test data generated in this role, be able to usefully analyse the data and use it to present technical reports.

Besides having a great work environment and the opportunity to bring exciting changes to the semiconductor world, we are also growing fast – so if you would like to play a key role in contributing to our success, please get in touch with us!

If you would like to find out more about Clas-SiC please visit our website: <https://www.clas-sic.com>.

**More about the Role:**

This is a new & key role which will help the Company continue to develop our demanding programmes for the delivery of power SiC diodes and MOSFETs. It will also be key in providing the characterisation required to tightly define the Company's Process Design Kit (PDK) solutions.

You will be a confident individual, data-driven and will possess a good understanding of semiconductor device physics, characterisation and reliability testing & analysis, and have the commitment to fully meet the challenges, responsibilities and obligations of this key role.

Reporting to the Engineering Manager, you will work mainly within the test engineering group to develop all the required test and reliability processes and procedures and software routines to assure the compliance of the Company's products.

**Personal Profile:**

- High-performing self-starter, who uses own initiative, is self-motivated and committed
- Displays tenacity with a strong work ethic
- Demonstrates strong communication/interpersonal skills
- A team player, willing to work with team members and able to recognise and utilise their contributions for the success of the Company.

**Education Requirements**

- Degree (or equivalent) in relevant Electrical/Electronic Engineering subject

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**Knowledge, Experience & Skills:**

- Good understanding of:
  - Semiconductor devices and the wafer/package level tests and test routines used to measure and characterise high voltage SiC MOSFET devices
  - Wafer/package level reliability tests and test routines required to assure the delivery of a reliable high voltage SiC MOSFET device
- Experienced in the use of manual & automatic wafer and package test hardware & software for high voltage / high current devices.
- Confident in programming in Python, VBA
- Understanding of GPIB / VISA interfacing
- Good grounding in project management principles and practices
- Competent in directly interfacing with customers and designers for both device development and ongoing production phases
- Experienced in Intellectual Property capture and protection
- Excellent IT skills (Microsoft Office, Data Storage and Analysis)
- Excellent communication skills, both written and verbal

**Key Responsibilities:**

- Interface with customers/project leads to understand & translate process requirements for new device projects
- Develop and maintain robust power device test procedures and routines
- Document processes & procedures so that they are easily understood and followed
- Train test operators in the use of test equipment
- To participate & occasionally lead Health and Safety activities within areas of responsibility
- Continually update your knowledge on the trends in SiC power device testing and reliability and where appropriate apply these learnings
- Input into the Clas-SiC technology roadmap
- Strive to continually simplify processes & improve quality
- Lower manufacturing costs with emphasis on yield improvement, reduction in variability and optimization of the factory
- Owning the supplier interface for tools within areas of responsibility
- Clearly and succinctly document and report development work to support the establishment of Clas-SiC's intellectual property
- Support project planning, monitoring and reporting

To apply, please submit your CV and covering letter to our HR Partner, Alison Melville at [hr@greigmelvillehr.co.uk](mailto:hr@greigmelvillehr.co.uk)

Closing date for applications: 26 March 2021